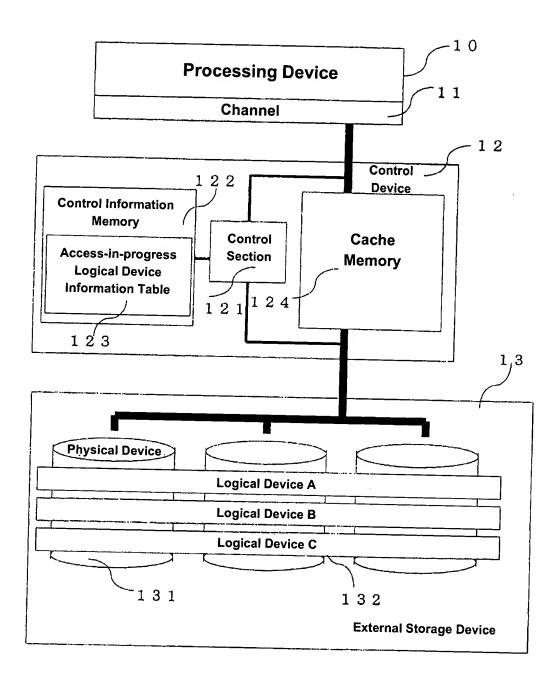
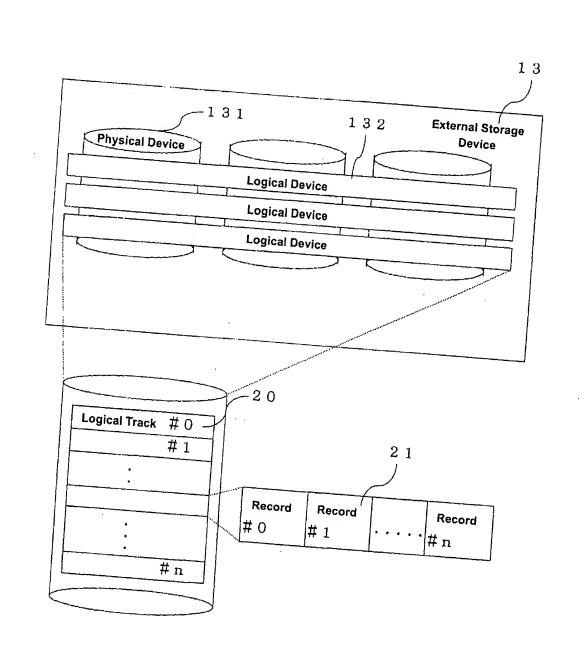
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Fig. 1



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Fig. 2



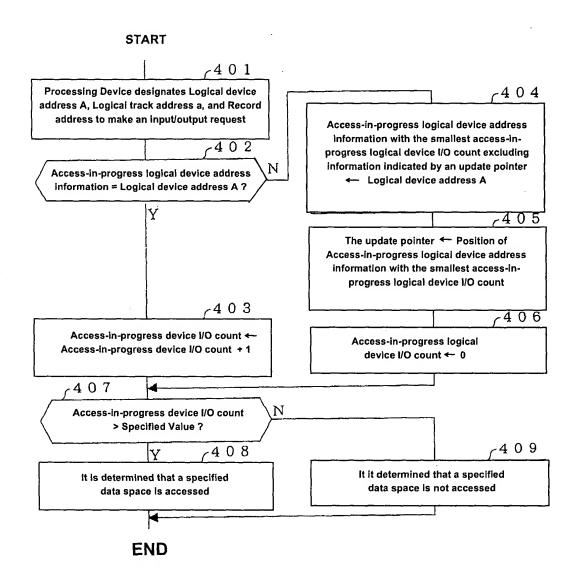
Hogan & Hartson 81940.0053 Isamu KUROKAWA et al. Information Processing System EV 325 216 981 US 7 Drawing Sheets; Sheet 3 of 7

Fig. 3

ccess-in-progr	ess Logical Device Infor	mation Table
	Update Pointer	
[0]	Device Address	1 / O Counter
[1]	Device Address	I / O Counter
[2]	Device Address	I / O Counter
- FY-17	Device Address	L/O Counter
[X-1]	Device Address	I / O Counter
[X]	Device Address	I / O Counter

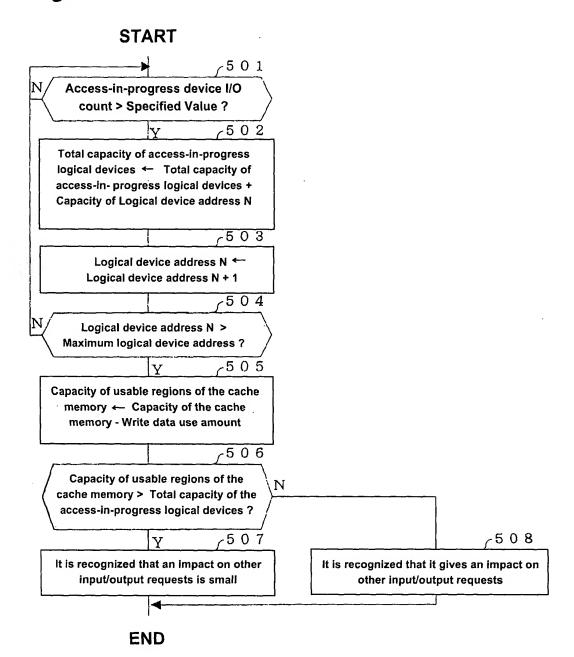
Hogan & Hartson 81940.0053 Isamu KUROKAWA et al. Information Processing System EV 325 216 981 US 7 Drawing Sheets; Sheet 4 of 7

Fig. 4



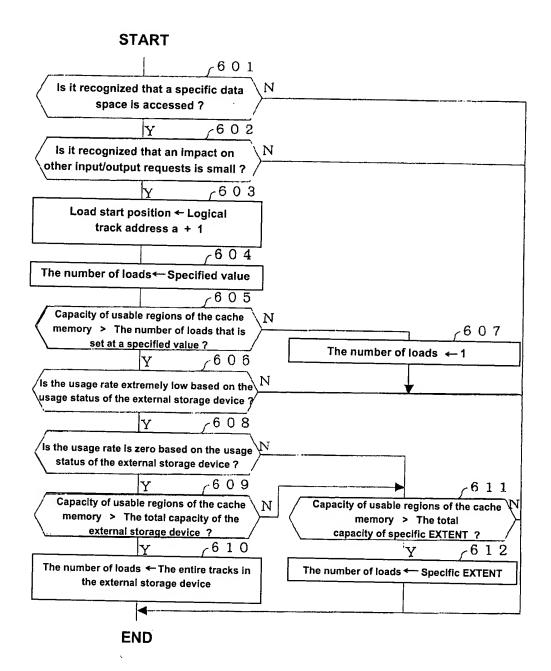
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Fig. 5



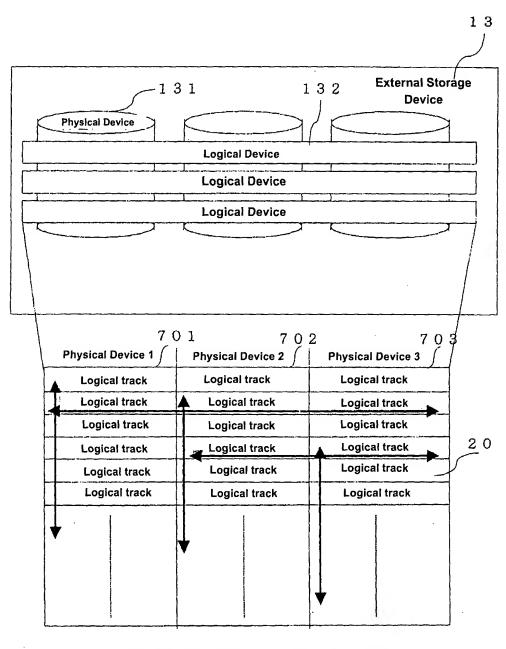
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Fig. 6



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Fig. 7



Arrows indicate directions of reading; they can be read in either a longitudinal or a horizontal direction